

REMARKS

Reconsideration is respectfully requested.

Claims 1-12 are currently pending. Claims 1-12 were rejected under 35 U.S.C. §112, first paragraph, as failing to comply with the enablement requirement. Claims 1-12 were rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 6,005,812 (Mullarkey). The Examiner objected to the Specification and to Claims 1-12 because of minor informalities.

This Response to the Office Action of April 11, 2006 is being made in accordance with a telephonic interview between Applicants' representative Victor A. Grossman and Examiner Quan Tra on July 11, 2006. The courtesy extended by the Examiner in conducting the telephonic interview is greatly appreciated. During the interview, the Examiner stated that the proposed amendments submitted on July 10, 2006 would overcome the rejections under 35 U.S.C. §112, first paragraph, of Claims 1-12. Accordingly, the Specification has been amended in accordance with these proposed amendments. Withdrawal of the rejection under 35 U.S.C. §112, first paragraph, of Claims 1-12 is respectfully requested.

Regarding the objections to the specification and the claims, the specification and Claim 1 have been amended. Accordingly, withdrawal of the objections to the specification and the claims is respectfully requested.

Regarding the rejection under 35 U.S.C. §102(b) of Claim 1, the Examiner states that Mullarkey teaches each and every limitation of Claim 1. After reviewing Mullarkey, it is respectfully submitted that the Examiner is incorrect.

First, the Examiner states Mullarkey teaches, in Column 5, Lines 6-11, the recitation of a plurality of local DC voltage generators, as recited by Claim 1. It is respectfully submitted that the Examiner is incorrect (i.e., not a plurality). Mullarkey teaches in Column 5, Lines 6-11 incorporating the (i.e., one) DRAM device 10 into any one of processor devices 84, 86, and 88.

One and only one device 10 is taught by Mullarkey. However, Mullarkey does not teach or suggest a plurality of local DC voltage generators distributed throughout the SOC chip, as recited in Claim 1.

Second, the Examiner states the test signal of Mullarkey discloses the clock control unit, as recited by Claim 1. However, the test control signal is merely a signal and not a control unit as recited in Claim 1. For example, with reference to FIG. 1 of Mullarkey, only a test signal as opposed to a clock control unit is shown. Moreover, as Mullarkey does not teach a clock control unit, Mullarkey cannot teach or suggest each local DC voltage generator receives a clock control signal from said clock control unit, as recited in Claim 1.

Third, the Examiner states that element 16 of Mullarkey discloses the power control unit, as recited by Claim 1, however, it is respectfully submitted that Mullarkey teaches element 16 is merely a ring oscillator which outputs a clock signal CLK (e.g., see, Column 3, Lines 1-11 and 24-29). Accordingly, Mullarkey cannot teach or suggest each local DC voltage generator receives a power control signal from said power control unit, as recited by Claim 1.

Fourth, amended Claim 1 includes the recitation wherein each local DC voltage generator receives a power control signal from said power control unit and a clock control signal from said clock control unit and outputs a reference voltage, said at least one regulator system receiving said reference voltage and outputting a pump control signal, the pump control signal being enabled and disabled in response to at least the clock control signal, which is neither taught nor suggested by Mullarkey.

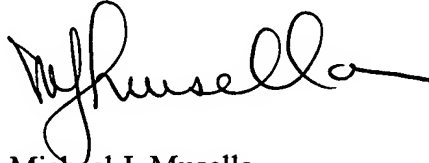
Accordingly, as Mullarkey does not teach each and every limitation of Claim 1, the Examiner cannot maintain a rejection under 35 U.S.C. §102(b). Withdrawal of the rejection under 35 U.S.C. §102(b) of Claim 1 is respectfully requested.

In light of the discussion above, it is respectfully submitted that independent Claim 1 overcomes the stated rejection and is in condition for allowance. Without conceding the

patentability per se of dependent Claims 2-12, it is respectfully submitted that these claims are also in condition for allowance by virtue of their dependence on Claim 1.

Claims 1-12 are believed to be in condition for allowance. Should the Examiner believe that a telephone conference or a personal interview would facilitate resolution of any remaining matters, the Examiner may contact Applicants' attorney at the number given below.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Musella", with a stylized flourish at the end.

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